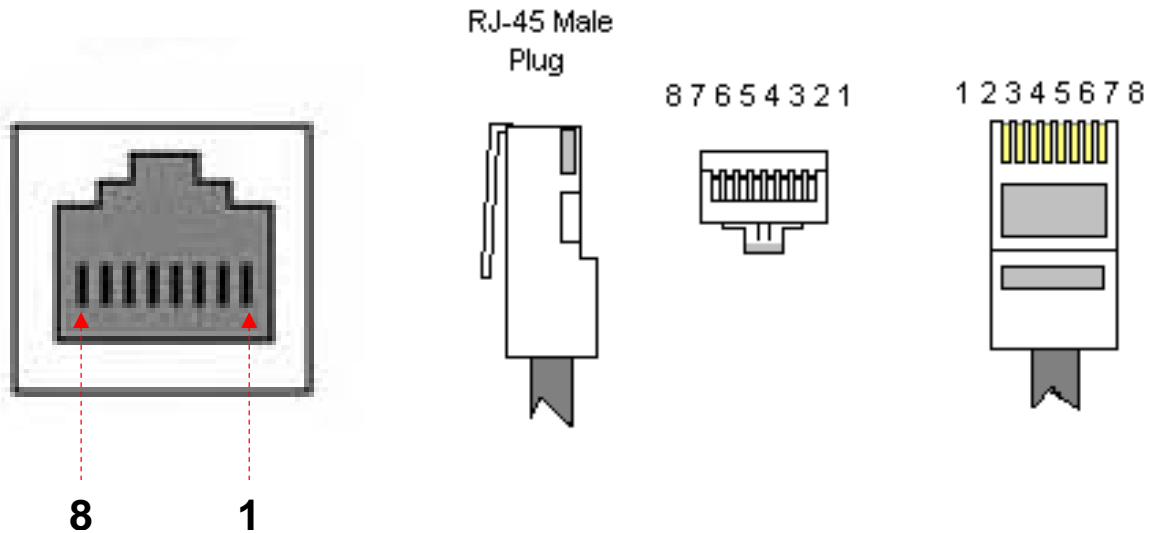










# La Voce DAC

## AQLINK schematic



RJ45 Pin#				
	Green/White Tracer	1	1 DALE	*1
	Green	2	2 MCLK	*2
	Orange/White Tracer	3	3 GND1	
	Blue	4	4 SCLK	
	Blue/White Tracer	5	5 GND2	
	Orange	6	6 LRCK	
	Brown/White Tracer	7	7 HL	*3
	Brown	8	8 DATA	

\* 1 = DALE: lead to logic level HIGH, or connect to pin 7 HL (H state) if the stream data is 16 BIT (eg. I2S from CDpro2 module), in case of stream 24 /32 BIT leave free (LOW state);

\* 2 = MCK (master clock) is not used with R2R DAC TDA1541, AD1865, PCM1704 modules;

\* 3 = pin 7 HL (H state): is an output signal of logic level H.

### CONFIGURATION JUMPER J8 (I2S LEN-SEL) on-board D276

This jumper must be configured according to the DAC module installed on the main board:

-TDA1541: Insert the jumper to 32;

-PCM1704, AD1865: Insert the jumper 16.